

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT APPLICATION of:

Nenand Rijavec

Appln. No.: 10/065,745

Art Unit: 2624

Filed: November 14, 2002

Examiner: Peter K. HUNTSINGER

For: APPARATUS, METHOD AND
PROGRAM PRODUCT FOR
CONTROLLING PRINTING

DECLARATION UNDER 37 C.F.R. §1.132

Mail Stop RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

I, Reinhard H. Hohensee, 3895 Norwood Ct, Boulder, CO 80304, hereby declare as follows:

I received a Bachelor of Engineering Science (BES) degree from S.U.N.Y. Stony Brook, New York, in 1970;

I received a Master of Science, Electrical Engineering (MSEE) degree from Syracuse University, Syracuse, New York, in 1972;

I received an Electrical Engineer (EE) degree from Syracuse University, Syracuse, New York, in 1977;

I joined IBM in 1973, where I worked until retirement in 2007.

While at IBM, I:

worked at IBM Development Laboratories in Endicott, NY and Boeblingen, Germany, where I had responsibility for development of disk and tape attachments;

was assigned to the IBM T.J. Watson Research Center working on prototypes for single-user workstations and inter-system communications; and

worked at IBM Printing Systems Division on the AFP/IPDS architecture, developing many extensions to AFP including support for N-up printing, Pre/Post-Processing, color, OpenType fonts, Unicode, and color management;

After retirement from IBM, in May 2007, I joined the InfoPrint Solutions Company as a Distinguished Engineer as lead architect for the AFP Architecture;

I am an author of more than two (2) technical publications and an inventor on more than twenty eight (28) patents and patent applications;

I have read U.S. Patent Application Serial No. 10/065,745 to Nenand Rijavec, published as Pub. No. 2004/0095596 A1, the present application (Application);

It is my professional opinion and understanding of the Application that:

Paragraph 0010 of the Application states that “[b]y using the design described here, each controller can be configured, by adding RIP machines and connecting networks, to suit the needs of each customer,”

Figure 2 shows a variable number of RIP processors (22a - 22n),

Therefore, the number of RIP processors, as well as the network connecting the RIP processors and the Sequencer (described in 0026), are dynamically configurable,

Customer A, for example, may require 7 RIP processors initially, but then may need to grow to 10 RIP processors; while similarly, customer B may have 12 RIP processors initially, but then cuts cost by dropping to 6 RIP processors,

While this may require reconfiguring the controller 10 ("each controller can be configured"), this does not require changing the Sequencer 21,

For example, the Sequencer 21 configuration indicates the number of RIP processors (N) attached and their respective network addresses, e.g., in memory or status registers that may or may not be resident in the Sequencer 21,

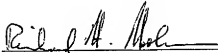
When RIP processors 22a – n are added to, or removed from, the controller 10, configuring the controller 10 updates this number N and the respective addresses, e.g., the sequencer may be designed to update automatically,

However, updating the Sequencer 21 configuration does not change the Sequencer 21, i.e., it does not require making structural changes to the Sequencer 21;

I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and

I further declare that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Affiant: Reinhard H. Hohensee

Signature:  Date: March 3, 2010

Residence: 3895 Norwood Ct, Boulder, CO 80304
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